



Features

ITU-T J.83 Annex A/C, DVB-C (ETS 300 429)
Compliant baseband transmitter for Cable Modem Termination Systems (CMTS)

- The MVD modulator cores can be delivered with an Intermediate Frequency output or a RF output when using Analog Devices or Maxim RF DACs (see separate datasheet, available on request)
- Drop-in module for Virtex-6™, Virtex-5™, Virtex-4™, Spartan-6™ and Spartan™-3/E/A FPGAs
- Single clock (up to 140 MHz+ for Spartan-3/6™, 180 MHz+ for Virtex-4/5/6™)
- Robust SPI input (discarding incorrect input packets)
- PCR re-stamping
- Supports programmable symbol rates
- Programmable 16, 32, 64, 128 and 256 QAM Symbol Mapping
- Intermediate frequency output for single DAC (14 bits) or baseband outputs (2 x 16 bits)
- Single / multi channel
- Fully synthesizable RTL VHDL design (not delivered) for easy customization
- Design delivered as Netlist
- MER > 42dB

Complete application fits into 3S500E and/or 3S400A depending on selected options



Resource Utilization

The core configuration may be set by conditional synthesis. Typical configuration with CPU interface.

	Slices	BRAMs (18k)	Mults/DSP48	BUFG	Deliverables : Datasheet and user's guide Netlist for core generation
Spartan 3/E/A	2 800	4	20	2	
Spartan 6	1 400	4	22	2	
Virtex 4	2 500	4	20	2	
Virtex 5	1 400	4	20	2	
Virtex 6	1 300	4	22	2	

(values may vary depending on implementation options)

Ordering information and related cores

Parameters	Designation
Fixed	MVD_DVBC_J83AC_FIXED_NET
GPIO programmable	MVD_DVBC_J83AC_GPIO_NET
CPU programmable	MVD_DVBC_J83AC_CPU_NET

VHDL source code : can be delivered as an option under NDA and other specific clauses

Complementary cores : DVB-C for AD9789 DAC, Upconverter for AD9739 DAC or MAX5881 DAC, contact us. For a multi-channel application, we recommend to use the AD9789 for 4 adjacent channels, or we recommend to use the AD9739 DAC or the MAX5881 DAC for more than 4 channels, or for non adjacent channels.

Related cores : Cable Modulator J83B, DVB-S, DVB-T/H, DVB Remultiplexer and/or ASI Receiver cores contact us at info_cores@mvd-fpga.com

Documentation and support : Datasheet and user's guide. In addition MVD can provide on site or remote coaching.