

Description

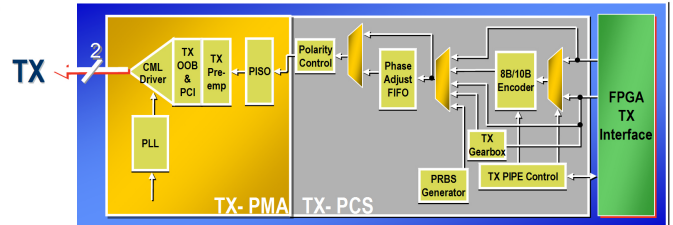
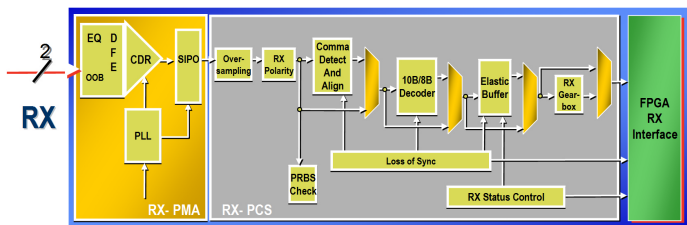
MVD Cores can develop any **high speed standard or proprietary link** including:

- Clock Configuration
- Clock/Data recovery
- Parallel/Serial, Serial/Parallel conversion
- Byte/Word alignment
- Channel Bonding
- Clock Correction
- Encoder/Decoder
- PRBS generator/checker
- Termination, Equalizer and Polarity Control

High-speed Serial Applications

- Chip-to-chip
- Computing
- Datacom
- Storage
- Telecom
- Video

These developments can be based on high speed protocols such as **PCIExpress, Aurora, Gigabit Ethernet**, etc.



Available technology

Device	Family	Transceiver type	Serial transceiver Max achievable rate (Gb/s)	Max number of transceivers per single device
Spartan-6™	LXT	GTP	3.125	8
Virtex-6™	LXT/SXT	GTX	6.6	36
Virtex-6™	HXT	GTX	6.6	48
Virtex-6™	HXT	GTH	11.1	24
Artix-7™	All	GTP	3.75	16
Kintex-7™	All	GTX	12.5	32
Virtex-7™	V and XC7VX485T	GTX	12.5	36 or 56
Virtex-7™	VX (except XC7VX485T)	GTH	13.1	96
Virtex-7™	VH	GTH	13.1	72
Virtex-7™	VH	GTZ	28.5	16

Deliverables : VHDL source code, Simulation Testbench

For more information, please contact us at info_cores@mvd-fpga.com